



1722
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Warren M. Farnworth

Serial No.: 09/944,488

Filed: August 30, 2001

For: METHODS AND APPARATUS FOR
STEREOLITHOGRAPHIC PROCESSING
OF COMPONENTS AND ASSEMBLIES

Examiner: E. Luk

Group Art Unit: 1722

Attorney Docket No.: 2269-3996US
(99-0254.00/US)

RECEIVED
DEC 11 2003
TC 1700

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

December 3, 2003
Date

Rachael M. Harris
Signature

Rachael M. Harris
Name (Type/Print)

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the documents listed on attached Form PTO-1449 or PTO/SB/08 be considered by the Examiner and made of record. Copies of the listed documents are enclosed pursuant to 37 C.F.R. § 1.98(a).

12/10/2003 MGEBRM1 00000016 09944488

01 FC:1806

180.00 0P

In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, no representation is made by Applicant herein that no other possible material information as defined in 37 C.F.R. § 1.56 (b) exists.

U.S. Patent Documents

<u>U.S. Patent No.</u>	<u>Publication Date</u>	<u>Patentee</u>
6,251,488	06/26/01	Miller et al.
6,259,962	07/10/01	Gothait
6,268,584	07/31/01	Keicher et al.
6,326,698	12/04/01	Akram
6,337,122	01/08/02	Grigg et al.
6,391,251	05/21/02	Keicher et al.
6,432,752	08/13/02	Farnworth
6,489,007	12/03/02	Grigg et al.
6,506,671	01/14/03	Grigg
6,514,798	02/04/03	Farnworth
6,524,346	02/25/03	Farnworth
6,525,408	02/25/03	Akram et al.
6,544,465	04/08/03	Farnworth et al.
6,544,821	04/08/03	Akram
6,544,902	04/08/03	Farnworth
6,548,897	04/15/03	Grigg
6,562,278	05/13/03	Farnworth et al.
6,569,753	05/27/03	Akram et al.
6,585,927	07/01/03	Grigg et al.
6,593,171	07/15/03	Farnworth
6,607,689	08/19/03	Farnworth
6,635,333	10/21/03	Grigg et al.

Other Documents

U.S. Patent Application Publication 2002/0066966 A1 to Farnworth, dated June 6, 2002

U.S. Patent Application Publication 2002/0098623 A1 to Akram, dated July 25, 2002

U.S. Patent Application Publication 2002/0105074 A1 to Akram et al., dated August 8, 2002

U.S. Patent Application Publication 2002/0171177 A1 to Kritchman et al., dated November 21, 2002

U.S. Patent Application Publication 2002/0182782 A1 to Farnworth, dated December 5, 2002

U.S. Patent Application Publication 2002/0195748 A1 to Farnworth, dated December 26, 2002

U.S. Patent Application Publication 2003/0003179 A1 to Farnworth et al., dated January 2, 2003

U.S. Patent Application Publication 2003/0003180 A1 to Farnworth et al., dated January 2, 2003

U.S. Patent Application Publication 2003/0003380 A1 to Farnworth et al., dated January 2, 2003

U.S. Patent Application Publication 2003/0003405 A1 to Farnworth et al., dated January 2, 2003

U.S. Patent Application Publication 2003/0068584 A1 to Farnworth et al., dated April 10, 2003

U.S. Patent Application Publication 2003/0072926 A1 to Grigg et al., dated April 17, 2003

U.S. Patent Application Publication 2003/0077418 A1 to Grigg et al., dated April 24, 2003

U.S. Patent Application Publication 2003/0089999 A1 to Akram, dated May 15, 2003

U.S. Patent Application Publication 2003/0092220 A1 to Akram, dated May 15, 2003

U.S. Patent Application Publication 2003/0093173 A1 to Farnworth et al., dated May 15, 2003

U.S. Patent Application Publication 2003/0098499 A1 to Akram et al., dated May 29, 2003

U.S. Patent Application Publication 2003/0102566 A1 to Farnworth, dated June 5, 2003

U.S. Patent Application Publication 2003/0129787 A1 to Farnworth, dated July 10, 2003

U.S. Patent Application Publication 2003/0139030 A1 to Grigg, dated July 24, 2003

U.S. Patent Application Publication 2003/0151167 A1 to Kritchman et al., dated August 14, 2003

U.S. Patent Application Publication 2003/0155693 A1 to Farnworth et al., dated August 21, 2003

U.S. Patent Application Publication 2003/0173665 A1 to Grigg, dated September 18, 2003

U.S. Patent Application Publication 2003/0176016 A1 to Grigg, dated September 18, 2003

U.S. Patent Application Publication 2003/0193092 A1 to Tan et al., dated October 16, 2003

U.S. Patent Application Publication 2003/0201531 A1 to Farnworth et al., dated October 30, 2003

U.S. Patent Application Publication 2003/0203158 A1 to Farnworth et al., dated October 30, 2003

U.S. Patent Application Publication 2003/0203612 A1 to Akram et al., dated October 30, 2003

U.S. Patent Application Publication 2003/0205849 A1 to Farnworth, dated November 6, 2003

MILLER et al., "Maskless Mesoscale Materials Deposition", Deposition Technology, September 2001, pages 20-22

MILLER, "New Laser-Directed Deposition Technology", Microelectronic Fabrication, August 2001, page 16

Webpage, Objet Prototyping the Future, "Objet FullCure700 Series", 1 page

Webpage, Objet Prototyping the Future, "How it Works", 2 pages

U.S. Patent Application No. 09/589,841, filed June 8, 2000, entitled "Stereolithographic Methods for Forming a Protective Layer on a Semiconductor Device Substrate and Substrates Including Protective Layers So Formed", inventor Farnworth et al.

U.S. Patent Application No. 09/590,646, filed June 8, 2000, entitled "Reinforced, Self-Aligning Conductive Structures for Semiconductor Device Components and Methods for Fabricating Same", inventor Williams et al.

U.S. Patent Application No. 09/651,930, filed August 31, 2000, entitled "Semiconductor Device Including Leads in Communication with Contact Pads Thereof and a Stereolithographically Fabricated Package Substantially Encapsulating the Leads and Methods for Fabricating the

Same", inventor Salman Akram

U.S. Patent Application No. 10/191,424, filed July 8, 2002, entitled "Semiconductor Devices with Permanent Polymer Stencil and Method for Manufacturing the Same", inventor Farnworth et al.

U.S. Patent Application No. 10/201,208, filed July 22, 2002, entitled "Thick Solder Mask for Confining Encapsulant Material Over Selected Locations of a Substrate, Assemblies Including the Solder Mask, and Methods", inventor Grigg et al.

U.S. Patent Application 10/370,755, filed February 20, 2003, entitled "Chip Scale Package Structures and Method of Forming Conductive Bumps Thereon", inventor Warren M. Farnworth

U.S. Patent Application No. 10/608,749, filed June 26, 2003, entitled "Methods for Labeling Semiconductor Device Components", inventor Grigg et al.

U.S. Patent Application No. 10/619,918, filed July 15, 2003, entitled "Stereolithographic Methods for Fabricating Hermetic Semiconductor Device Packages and Semiconductor Devices Including Stereolithographically Fabricated Hermetic Packages", inventor Warren M. Farnworth

U.S. Patent Application No. 10/642,908, filed August 18, 2003, entitled "Solder Masks for Use on Carrier Substrates, Carrier Substrates and Semiconductor Device Assemblies Including Such Solder Masks, and Methods", inventor Tan et al.

U.S. Patent Application No. 10/663,402, filed September 16, 2003, entitled "Processes for Facilitating Removal of Stereolithographically Fabricated Objects from Platens of Stereolithographic Fabrication Equipment, Object Release Elements for Effecting Such Processes, Systems and Fabrication Processes Employing the Object Release Elements, and Objects Which Have Been Fabricated Using the Object Release Elements", inventor Farnworth et al.

U.S. Patent Application No. 10/688,354, filed October 17, 2003, entitled "Thick Solder Mask for Confining Encapsulant Material Over Selected Locations of a Substrate and Assemblies Including the Solder Mask", inventor Grigg et al.

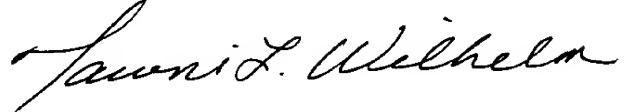
U.S. Patent Application No. 10/690,417, filed October 20, 2003, entitled "Methods of Coating and Singulating Wafers and Chip-Scale Packages Formed Therefrom", inventor Farnworth et al.

Applicant offers to supply any explanation or discussion of the documents which the Examiner feels is necessary or desirable and which is requested.

This Supplemental Information Disclosure Statement is filed after the mailing date of the first Office Action on the merits.

The fee pursuant to 37 C.F.R. § 1.17(p) is enclosed.

Respectfully submitted,



Tawni L. Wilhelm
Registration No. 47,456
Attorney for Applicant(s)
TRASKBRITT
P.O. Box 2550
Salt Lake City, Utah 84110-2550
Telephone: 801-532-1922

Date: December 3, 2003

TLW/sls:rmh

Enclosures: Form PTO-1449 or PTO/SB/08
Copy of documents cited
Check No. 5431 in the amount of \$180.00

Document in ProLaw



Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Shee

1

T

<i>Complete if Known</i>	
Application Number	09/944,488
Filing Date	August 30, 2001
First Named Inventor	Warren M. Farnsworth
Group Art Unit	1722
Examiner Name	E. Luk
Attorney Docket Number	3996US (99-0254.00/US)

RECEIVED
DEC 11 2003

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
		US-6,251,488	06/26/01	Miller et al.	
		US-6,259,962	07/10/01	Gothait	
		US-6,268,584	07/31/01	Keicher et al.	
		US-6,326,698	12/04/01	Akram	
		US-6,337,122	01/08/02	Grigg et al.	
		US-6,391,251	05/21/02	Keicher et al.	
		US-6,432,752	08/13/02	Farnworth	
		US-6,489,007	12/03/02	Grigg et al.	
		US-6,506,671	01/14/03	Grigg	
		US-6,514,798	02/04/03	Farnworth	
		US-6,524,346	02/25/03	Farnworth	
		US-6,525,408	02/25/03	Akram et al.	
		US-6,544,465	04/08/03	Farnworth et al.	
		US-6,544,821	04/08/03	Akram	
		US-6,544,902	04/08/03	Farnworth	
		US-6,548,897	04/15/03	Grigg	
		US-6,562,278	05/13/03	Farnworth et al.	
		US-6,569,753	05/27/03	Akram et al.	
		US-6,585,927	07/01/03	Grigg et al.	
		US-6,593,171	07/15/03	Farnworth	
		US-6,607,689	08/19/03	Farnworth	
		US-6,635,333	10/21/03	Grigg et al.	

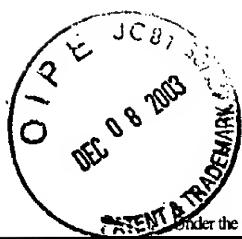
FOREIGN PATENT DOCUMENTS

Examiner Signature		Date Considered	
-----------------------	--	--------------------	--

***EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



PTO/SB/08B(10-01)

Approved for use through 10/31/2002. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet

2

of

6

Complete if Known

Application Number	09/944,488
Filing Date	August 30, 2001
First Named Inventor	Warren M. Farnworth
Group Art Unit	1722
Examiner Name	E. Luk
Attorney Docket Number	3996LIS (99-0254.00/LIS)

DEC 11 2003
TC 1700

RECEIVED

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		U.S. Patent Application Publication 2002/0066966 A1 to Farnworth, dated June 6, 2002	
		U.S. Patent Application Publication 2002/0098623 A1 to Akram, dated July 25, 2002	
		U.S. Patent Application Publication 2002/0105074 A1 to Akram et al., dated August 8, 2002	
		U.S. Patent Application Publication 2002/0171177 A1 to Kritchman et al., dated November 21, 2002	
		U.S. Patent Application Publication 2002/0182782 A1 to Farnworth, dated December 5, 2002	
		U.S. Patent Application Publication 2002/0195748 A1 to Farnworth, dated December 26, 2002	
		U.S. Patent Application Publication 2003/0003179 A1 to Farnworth et al., dated January 2, 2003	
		U.S. Patent Application Publication 2003/0003180 A1 to Farnworth et al., dated January 2, 2003	
		U.S. Patent Application Publication 2003/0003380 A1 to Farnworth et al., dated January 2, 2003	
		U.S. Patent Application Publication 2003/0003405 A1 to Farnworth et al., dated January 2, 2003	
		U.S. Patent Application Publication 2003/0068584 A1 to Farnworth et al., dated April 10, 2003	
		U.S. Patent Application Publication 2003/0072926 A1 to Grigg et al., dated April 17, 2003	

Examiner Signature		Date Considered
--------------------	--	-----------------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



PTO/SB/08B(10-01)

Approved for use through 10/31/2002. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number

+
RECEIVED
TC 1700
DEC 11 2003

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet

3

of

6

Complete if Known

Application Number	09/944,488
Filing Date	August 30, 2001
First Named Inventor	Warren M. Farnworth
Group Art Unit	1722
Examiner Name	E. Luk
Attorney Docket Number	3996LIS (99-0254.00/LIS)

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS		
Examiner Initials *	Cite No. ¹	
		Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
		U.S. Patent Application Publication 2003/0077418 A1 to Grigg et al., dated April 24, 2003
		U.S. Patent Application Publication 2003/0089999 A1 to Akram, dated May 15, 2003
		U.S. Patent Application Publication 2003/0092220 A1 to Akram, dated May 15, 2003
		U.S. Patent Application Publication 2003/0093173 A1 to Farnworth et al., dated May 15, 2003
		U.S. Patent Application Publication 2003/0098499 A1 to Akram et al., dated May 29, 2003
		U.S. Patent Application Publication 2003/0102566 A1 to Farnworth, dated June 5, 2003
		U.S. Patent Application Publication 2003/0129787 A1 to Farnworth, dated July 10, 2003
		U.S. Patent Application Publication 2003/0139030 A1 to Grigg, dated July 24, 2003
		U.S. Patent Application Publication 2003/0151167 A1 to Kritchman et al., dated August 14, 2003
		U.S. Patent Application Publication 2003/0155693 A1 to Farnworth et al., dated August 21, 2003
		U.S. Patent Application Publication 2003/0173665 A1 to Grigg, dated September 18, 2003

Examiner Signature	Date Considered
--------------------	-----------------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



+

RECEIVED
DEC 17 2003
10-1700

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet

4

of

6

Complete if Known

Application Number	09/944,488
Filing Date	August 30, 2001
First Named Inventor	Warren M. Farnworth
Group Art Unit	1722
Examiner Name	E. Luk
Attorney Docket Number	39961US (99-0254.00/US)

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS		
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
		U.S. Patent Application Publication 2003/0176016 A1 to Grigg, dated September 18, 2003
		U.S. Patent Application Publication 2003/0193092 A1 to Tan et al., dated October 16, 2003
		U.S. Patent Application Publication 2003/0201531 A1 to Farnworth et al., dated October 30, 2003
		U.S. Patent Application Publication 2003/0203158 A1 to Farnworth et al., dated October 30, 2003
		U.S. Patent Application Publication 2003/0203612 A1 to Akram et al., dated October 30, 2003
		U.S. Patent Application Publication 2003/0205849 A1 to Farnworth, dated November 6, 2003
		MILLER et al., "Maskless Mesoscale Materials Deposition", Deposition Technology, September 2001, pages 20-22
		MILLER, "New Laser-Directed Deposition Technology", Microelectronic Fabrication, August 2001, page 16
		Webpage, Objet Prototyping the Future, "Objet FullCure700 Series", 1 page
		Webpage, Objet Prototyping the Future, "How it Works", 2 pages
Examiner Signature		Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet

5

of

6

Complete if Known

Application Number	09/944,488
Filing Date	August 30, 2001
First Named Inventor	Warren M. Farnworth
Group Art Unit	1722
Examiner Name	E. Luk

Attorney Docket Number 3996US (99-0254 00/US)

RECEIVED
DEC 11 2003
TC 1700

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		U.S. Patent Application No. 09/589,841, filed June 8, 2000, entitled "Stereolithographic Methods for Forming a Protective Layer on a Semiconductor Device Substrate and Substrates Including Protective Layers So Formed", inventor Farnworth et al.	
		U.S. Patent Application No. 09/590,646, filed June 8, 2000, entitled "Reinforced, Self-Aligning Conductive Structures for Semiconductor Device Components and Methods for Fabricating Same", inventor Williams et al.	
		U.S. Patent Application No. 09/651,930, filed August 31, 2000, entitled "Semiconductor Device Including Leads in Communication with Contact Pads Thereof and a Stereolithographically Fabricated Package Substantially Encapsulating the Leads and Methods for Fabricating the Same", inventor Salman Akram	
		U.S. Patent Application No. 10/191,424, filed July 8, 2002, entitled "Semiconductor Devices with Permanent Polymer Stencil and Method for Manufacturing the Same", inventor Farnworth et al.	
		U.S. Patent Application No. 10/201,208, filed July 22, 2002, entitled "Thick Solder Mask for Confining Encapsulant Material Over Selected Locations of a Substrate, Assemblies Including the Solder Mask, and Methods", inventor Grigg et al.	
		U.S. Patent Application 10/370,755, filed February 20, 2003, entitled "Chip Scale Package Structures and Method of Forming Conductive Bumps Thereon", inventor Warren M. Farnworth	
		U.S. Patent Application No. 10/608,749, filed June 26, 2003, entitled "Methods for Labeling Semiconductor Device Components", inventor Grigg et al.	
		U.S. Patent Application No. 10/619,918, filed July 15, 2003, entitled "Stereolithographic Methods for Fabricating Hermetic Semiconductor Device Packages and Semiconductor Devices Including Stereolithographically Fabricated Hermetic Packages", inventor Warren M. Farnworth	
		U.S. Patent Application No. 10/642,908, filed August 18, 2003, entitled "Solder Masks for Use on Carrier Substrates, Carrier Substrates and Semiconductor Device Assemblies Including Such Solder Masks, and Methods", inventor Tan et al.	
Examiner Signature		Date Considered	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



PTO/SB/08B(10-01)

Approved for use through 10/31/2002. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number

+

RECEIVED

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet

6

of

6

Complete if Known

Application Number	09/944,488
Filing Date	August 30, 2001
First Named Inventor	Warren M. Farnworth
Group Art Unit	1722
Examiner Name	E. Luk

Attorney Docket Number

DEC 11 2003
TC 1700

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		U.S. Patent Application No. 10/663,402, filed September 16, 2003, entitled "Processes for Facilitating Removal of Stereolithographically Fabricated Objects from Platens of Stereolithographic Fabrication Equipment, Object Release Elements for Effecting Such Processes, Systems and Fabrication Processes Employing the Object Release Elements, and Objects Which Have Been Fabricated Using the Object Release Elements", inventor Farnworth et al.	
		U.S. Patent Application No. 10/688,354, filed October 17, 2003, entitled "Thick Solder Mask for Confining Encapsulant Material Over Selected Locations of a Substrate and Assemblies Including the Solder Mask", inventor Grigg et al.	
		U.S. Patent Application No. 10/690,417, filed October 20, 2003, entitled "Methods of Coating and Singulating Wafers and Chip-Scale Packages Formed Therefrom", inventor Farnworth et al.	
Examiner Signature		Date Considered	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.